

PATENT NUMBER

## U.S. UTILITY Patent Application

I.P.E. A.K. T.M. SCANNED T.M. O.A. C.H.	PATENT DATE
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APPLICATION NO. 09/670917	CONT/PRIOR F	CLASS 438	SUBCLASS 791	ART UNIT <del>2810</del> 2822	EXAMINER Juerrero
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 Kiyohiko Maeda

TITLE: BEST AVAILABLE COPY  
 Semiconductor device manufacturing method and semiconductor  
 manufacturing apparatus

PTO-2040  
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<b>ISSUING CLASSIFICATION</b>								
<b>ORIGINAL</b>		<b>CROSS REFERENCE(S)</b>						
<b>CLASS</b>	<b>SUBCLASS</b>	<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>					
<b>INTERNATIONAL CLASSIFICATION</b>								

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b> Total Claims:      Print Claim for O.G.	
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent, No. _____	_____ (Assistant Examiner)      _____ (Date)		<b>ISSUE FEE</b>	
	_____ (Primary Examiner)      _____ (Date)		Amount Due	Date Paid
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